



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al. Art Unit : 2811
Serial No. : 10/080,831 Examiner : J. Nguyen
Filed : February 21, 2002
Title : SEMICONDUCTOR DEVICE AND PATTERN LAYOUT METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR ONE-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated August 20, 2003, be extended for one month to and including December 20, 2003.

Enclosed is a check for \$110 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 12/19/03

Arthur Ortega
Arthur Ortega
Reg. No. 53,422

Fish & Richardson P.C.
45 Rockefeller Plaza, Suite 2800
New York, New York 10111
Telephone: (212) 765-5070
Facsimile: (212) 258-2291

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110.00 DP

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

DECEMBER 19, 2003

Date of Deposit

Patricia F. Suchanek
Signature

PATRICIA F. SUCHANEK
Typed or Printed Name of Person Signing Certificate